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| AT1 | IA 1 |
| Restoring division | Booth’s algorithm flowchart and problems |
| Non Restoring division | IEEE 754 Floating point representation format- explanation and problems. |
| Basic gates - description , truth table and logic diagram | Differentiate between Programmed I/O and Interrupt Driven I/O |
| Half and full adder - description , truth table and logic diagram, logical expression. | Programmed I/O |
| Pipeline hazards | Interrupt Driven I/O |
| DMA | Memory hierarchy |
| Classification of computer memory | Memory parameters |
| Instruction pipelining | Cache memory mapping techniques |
| nanoprogramming | SRAM DRAM & difference |
| Cache coherence protocols | Decimal to binary conversion –step wise problem solving |
| Hard wired and microprogrammed approah | Binary to Decimal conversion –step wise problem solving |
|  | Half and full adder & difference between them |
|  | Difference between Multiplexer and DeMultiplexer |
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